MCF VME MAP

Address	Function	Valid	Read/	Function
24 bit HEX	Name	Data	Write	Description
0x800100	CSR0	D[7..0]	R/W	General Control/Status Register, currently diagnostic only
0x800200	/RST_BRD	N/A	W	Reset Board command
0x8N0000	FANOUT N CSR1	D[7..0]	R/W	Fanout N control/status register, currently diagnostic only
0x8N0800	FANOUT N PHASE[4..0]	D[4..0]	R/W	Fanout N Phase Adjustment, 24 steps of 785pS
0x8N1000	FANOUT N SC CSR	D[7..0]	R/W	Fanout N Slow Control MTM Command download register
0x8N1800	FANOUT N HL TEST DATA	D[7..0]	R/W	Fanout N HOTLink Test Data Download Register
0x8N2000	FANOUT N HL SEND TEST DATA	N/A	W	Fanout N HOTLink Send Test Data Command
0x8N8000	FANOUT N SC MEMORY RESERVED	D[7..0]	R/W	Reserved
0x8N8800	FANOUT N SC MEMORY SGATE_DEL[3..0] SET	D[3..0]	R/W	Fanout N SGATE coarse delay setting
0x8N9000	FANOUT N SC MEMORY CNTRST_DEL[3..0] SET	D[3..0]	R/W	Fanout N CNTRST coarse delay setting
0x8N9800	FANOUT N SC MEMORY TCAL_DEL[3..0] SET	D[3..0]	R/W	Fanout N TCAL coarse delay setting
0x8NA000	FANOUT N SC MEMORY REFRAME, /BISTEN, PHASE_INV	D[2..0]	R/W	Fanout N diagnostic settings
0x8NA800	FANOUT N SC MEMORY RESERVED	D[7..0]	R/W	Reserved
0x8NB000	FANOUT N SC MEMORY RESERVED	D[7..0]	R/W	Reserved
0x8NB800	FANOUT N SC MEMORY RESERVED	D[7..0]	R/W	Reserved
0x8NC000	FANOUT N SC MEMORY RESERVED	D[7..0]	R/W	Reserved
0x8NC800	FANOUT N SC MEMORY SGATE_DEL[3..0] STATUS	D[3..0]	R/W	Fanout N SGATE coarse delay readback from MTM/VTM
0x8ND000	FANOUT N SC MEMORY CNTRST_DEL[3..0] STATUS	D[3..0]	R/W	Fanout N CNTRST coarse delay readback from MTM/VTM
0x8ND800	FANOUT N SC MEMORY TCAL_DEL[3..0] STATUS	D[3..0]	R/W	Fanout N TCAL coarse delay readback from MTM/VTM
0x8NE000	FANOUT N SC MEMORY REFRAME, /BISTEN, PHASE_INV ST	D[7..0]	R/W	Fanout N diagnostic status readback from MTM/VTM
0x8NE800	FANOUT N SC MEMORY HL_TESTD[3..0] READBACK	D[7..0]	R/W	Fanout N HOTLink Diagnostic Data readback from MTM/VTM
0x8NF000	FANOUT N SC MEMORY MTM PAR_ERR, HL_ERR	D[7..0]	R/W	Fanout N error readback from MTM/VTM
0x8NF800	FANOUT N SC MEMORY RESERVED	D[7..0]	R/W	Reserved

N = 4 bit fanout select address (12 addresses, 0 through B)
SC = Slow Control